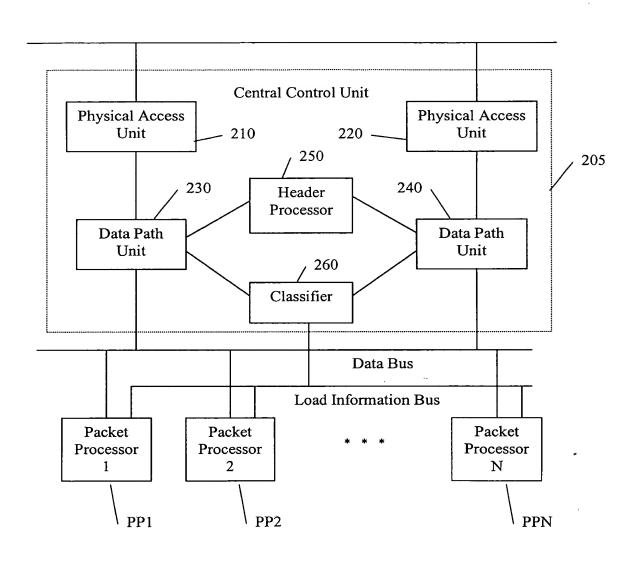


Fig. 1



200

Fig. 2

Processing Load Information	Processing Load	Action
00	Unavailable	Assign no new process flows to processor.
01	Heavily Loaded	Assign only a few more new process flows to processor.
10	Moderately Loaded	Assign new process flows to processor occasionally.
11	Lightly Loaded	Assign new process flows to processor as necessary.

Fig. 3

	us	ng				us	m <sup>c</sup>	
	Assign	Assign	X	X	X	Assign	Assign	×
Nound Inree	Assign	X	Assign	Assign	X	Assign	X	Assign
Kouna 1 wo	Assign	Assign	X	X	X	Assign	Assign	X
Nouna One	Assign	X	X	X	X	Assign	X	×
Information	11	10	01	01	00	11	10	01
racket rrocessor	Processor PP1	Processor PP2	Processor PP3	Processor PP4	Processor PP5	Processor PP6	Processor PP7	Processor PP8

Fig. 4

Processing Load Percentage	Processing Load	Action
75% to 100%	Unavailable	Assign no new process flows to processor.
50% to 74%	Heavily Loaded	Assign only a few more new process flows to processor.
25% to 49%	Moderately Loaded	Assign new process flows to processor occasionally.
0% to 24%	Lightly Loaded	Assign new process flows to processor as necessary.

Fig. 5

Idle Time	Processing Load	Action
Less Than Time	Unavailable	Assign no new process flows to processor.
Period A		·
Greater Than Time	Heavily Loaded	Assign only a few more new process
Period A And Less	-	flows to processor.
Than Time Period B		
Greater Than Time	Moderately Loaded	Assign new process flows to processor
Period B And Less		occasionally.
Than Time Period C		
Greater Than Time	Lightly Loaded	Assign new process flows to processor as
Period C		necessary.

Fig. 6

Data Rate At Which Data Supplied To Processor	Processing Load	Action
Greater Than Data Rate A	Unavailable	Assign no new process flows to processor.
Less Than Data Rate A And Greater Than Data Rate B	Heavily Loaded	Assign only a few more new process flows to processor.
Less Than Data Rate B And Greater Than Data Rate C	Moderately Loaded	Assign new process flows to processor occasionally.
Less Than Data Rate C	Lightly Loaded	Assign new process flows to processor as necessary.

Fig. 7

1